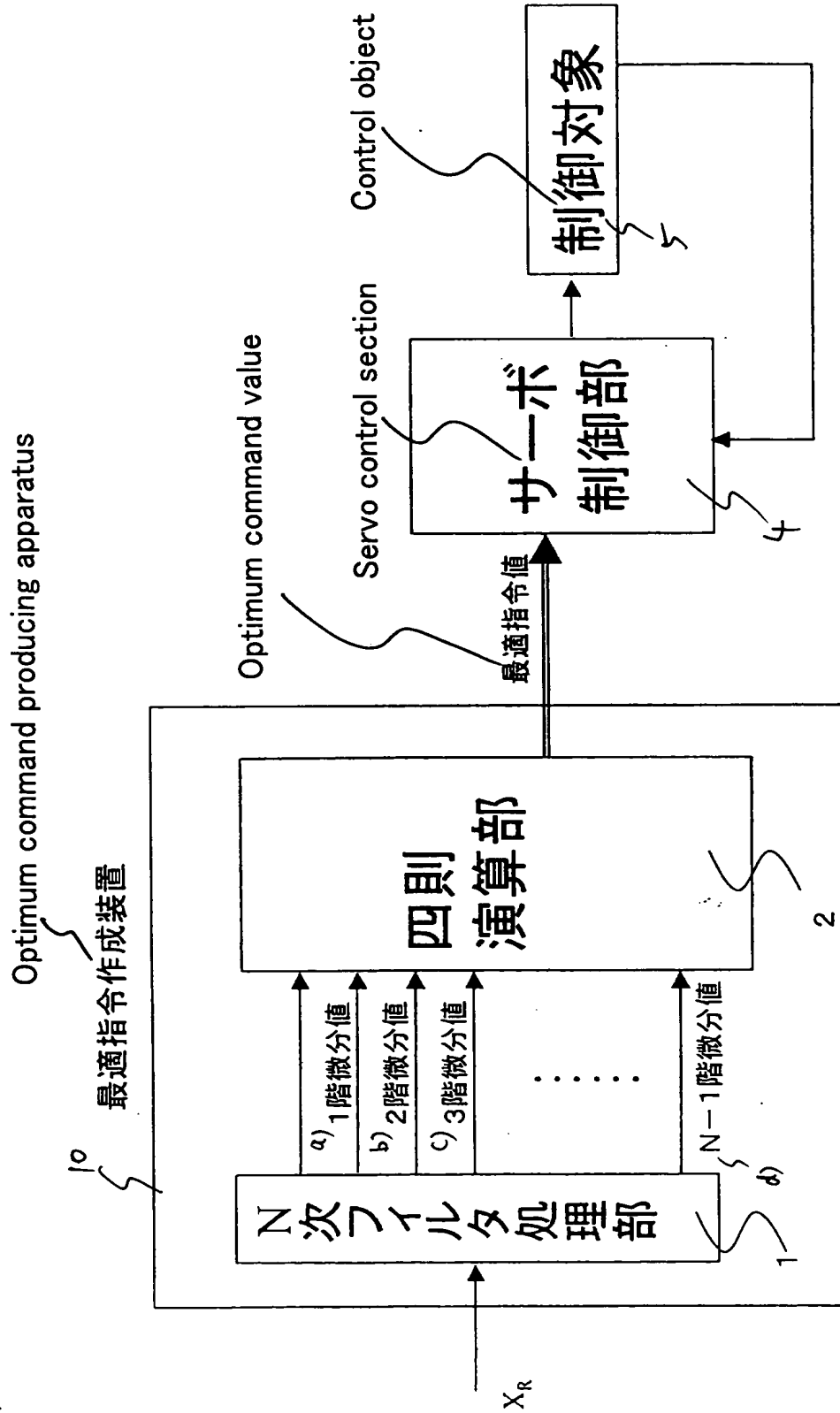


Fig. 1

- a) 1-rank differential value
- b) 2-rank differential value
- c) 3-rank differential value
- d) (N-1)-rank differential value



- 1) N-order filter processing section
- 2) Arithmetic unit

- 1-rank differential value
- 2-rank differential value
- 3-rank differential value
- (N-1)-rank differential value

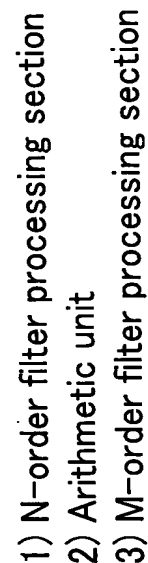
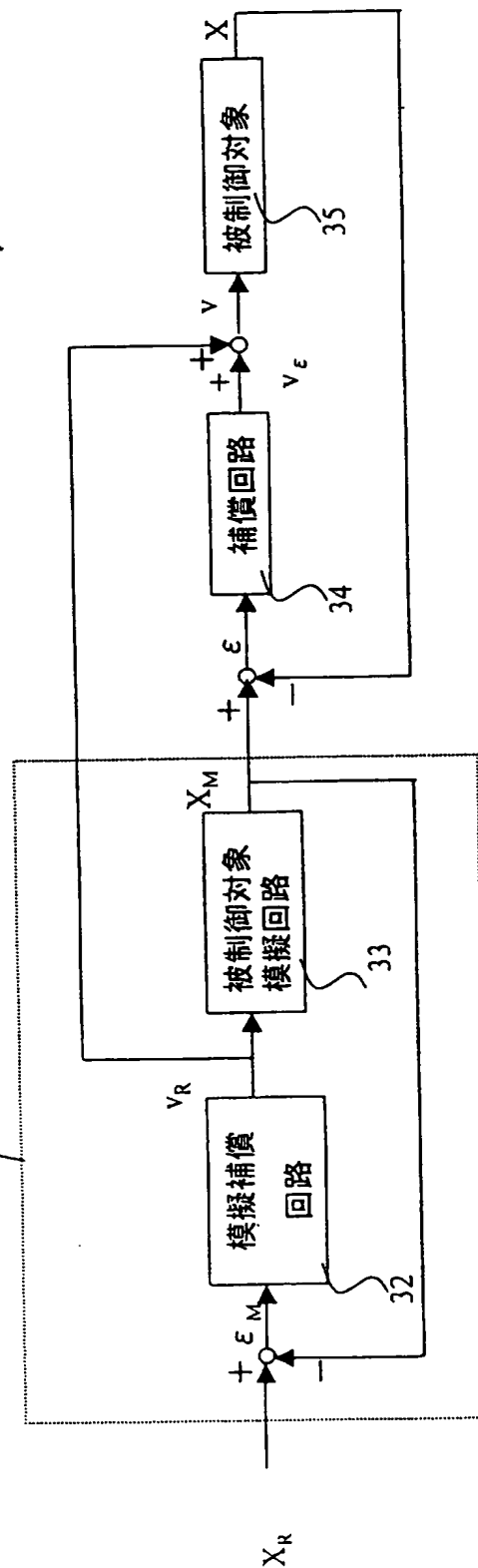


Fig. 3

31 模擬フィードバック制御装置

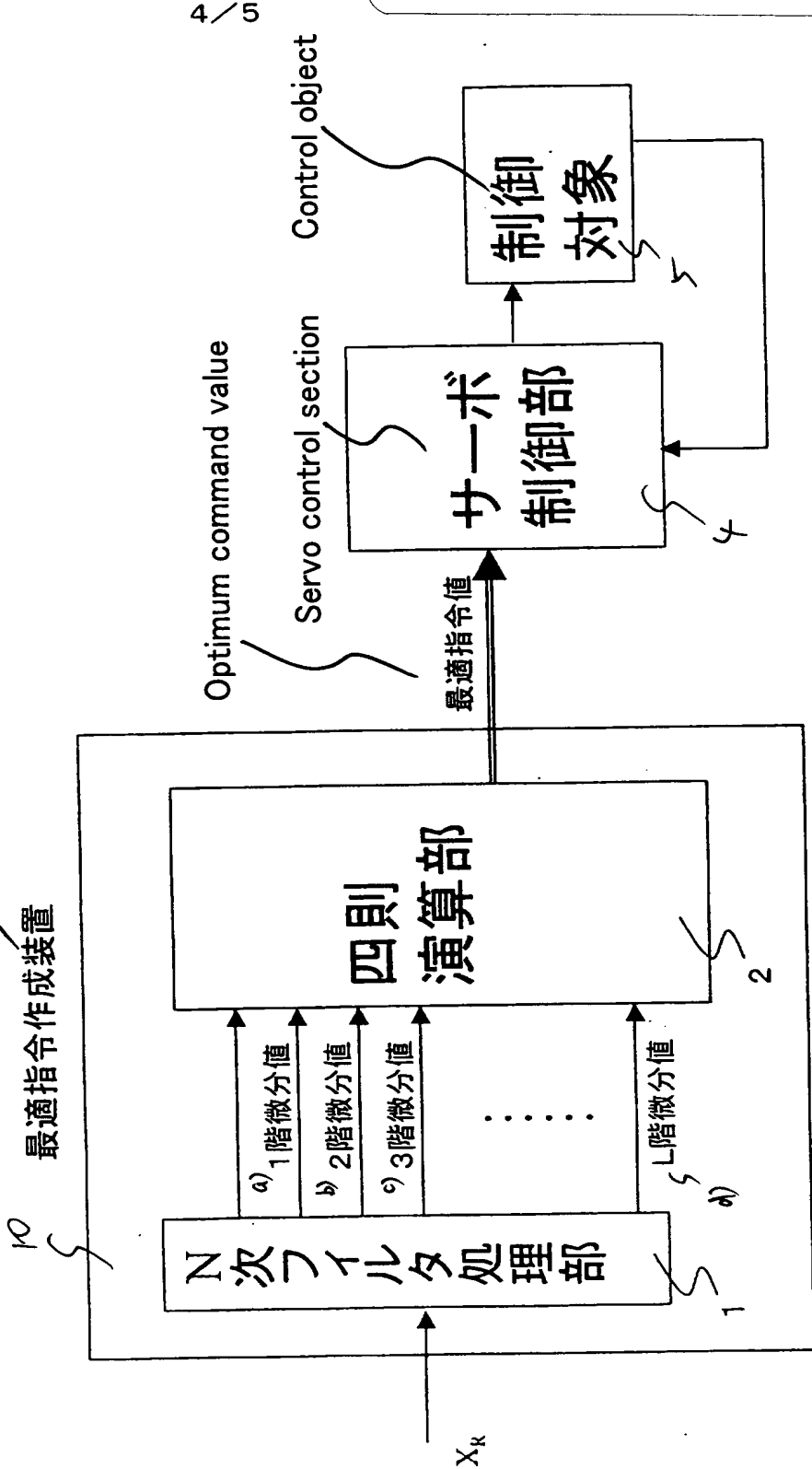


- (31) simulation feedback control apparatus
- (32) simulation compensating circuit
- (33) controlled object simulating circuit
- (34) compensating circuit
- (35) controlled object

Fig. 4

- a) 1-rank differential value
- b) 2-rank differential value
- c) 3-rank differential value
- d) L-rank differential value

Optimum command producing apparatus



- 1) N-order filter processing section
- 2) Arithmetic unit

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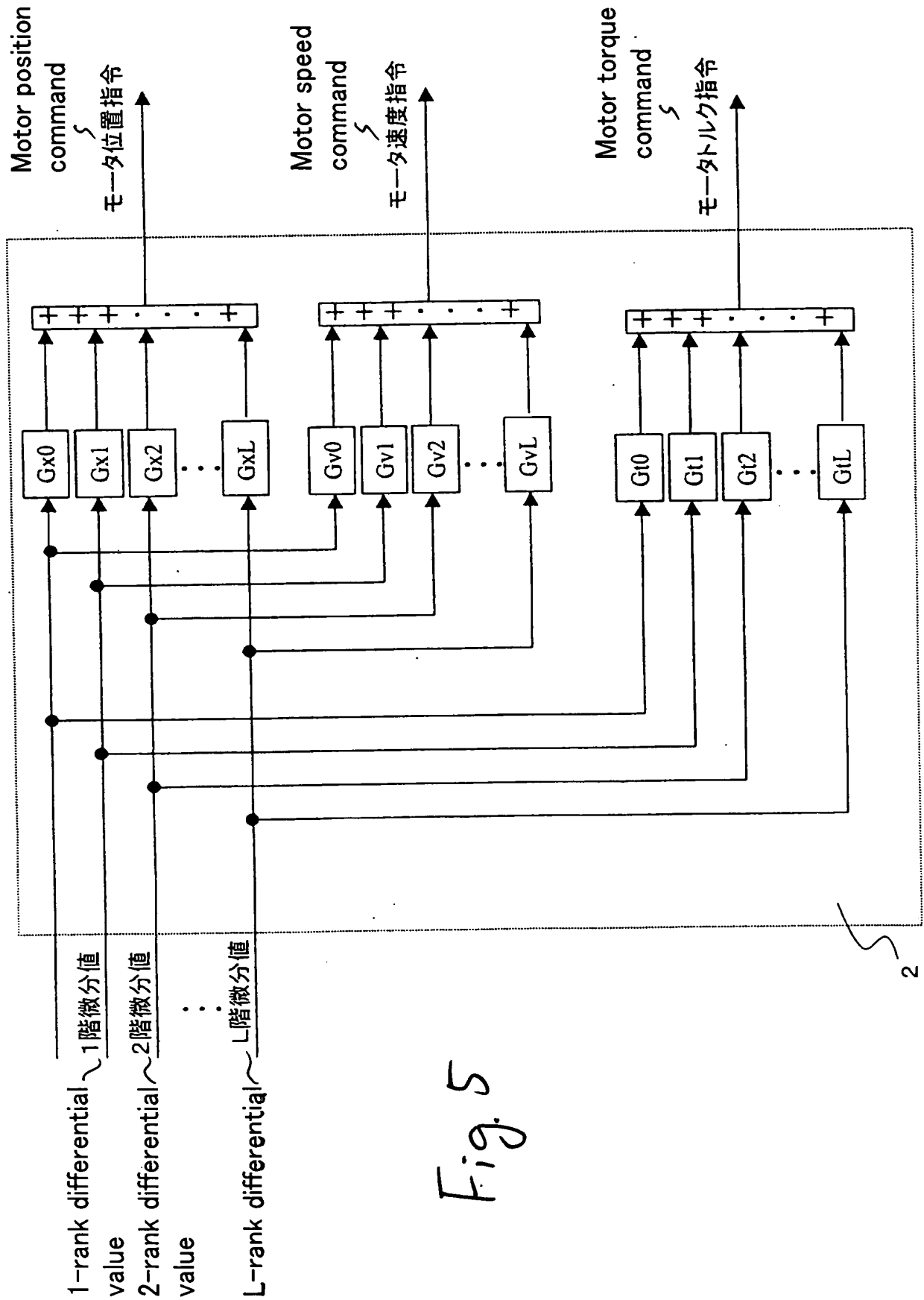


Fig. 5